

<b>Notice of References Cited</b>	Application/Control No. 09/679,456	Applicant(s)/Patent Under Reexamination RAGO, STEPHEN A.	
	Examiner Jean M. Corrielus	Art Unit 2162	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,768,532	06-1998	Megerian, Mark Gregory	709/245
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Andras et al., "An XML based framework for self-describing parallel I/O data", IEEE, 2003
	V	HDF5, User's guide: Data model, file structure", Chapter 1.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.